

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	Dbs	Default Operator	Plurals	Time Stamp
L3	0	ioput and @pd>"20081001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/09/15 16:45
L4	8	(bidirectional adj node) and @pd>"20081001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/09/15 16:46
L5	41	(analog and digital and bidirectional and ((mixed near signal) or mixed-signal) and simulation) and @pd>"20081001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/09/15 16:48
L6	269	703/14.ccls. and @pd>"20081001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2009/09/15 16:50

## EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L8	1814	(node and digital and analog).clm.	US-PGPUB	OR	ON	2009/09/15 17:18
L9	368	(node and digital and analog and state).clm.	US-PGPUB	OR	ON	2009/09/15 17:18
L10	9	(node and digital and analog and state and (high adj impedance)).clm.	US-PGPUB	OR	ON	2009/09/15 17:18

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Did you mean: **input**

### Voltage level translation for an output driver system with a bias generator

F Hinedi, M Cases, S Dutta, RH Dennard - US Patent 5,986,472, 1999 - Google Patents  
... A gate of transistor 118 is connected to bias circuit 130 at Node E. A drain of transistor 118 is connected to an information node **IOPUT**, which is connected to ...

Cited by 3 - [Related articles](#) - All 2 versions

### Prediction of average cutting size while drilling shales

A Hayatdavoudi, NC Durugbor, A Ghalambor, ... - SPE/IADC Drilling Conference, 1987 - onepetro.org  
... betwsa power **input**-output ead average cutting qiee for both 8 1/2 end 6 5/8 iocb drill bits. ... lodeed, field the bit w **input** mt bto ock-bit interfee eod ...

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### Discrete adaptive control: A sufficient condition for stability and applications

JJ Fuchs - IEEE Transactions on Automatic Control, 1980 - ieeexplore.ieee.org  
... we-**input** singleootpnt systems is conddered ming a model-reference type appro&. A sufficient condition upon the adaptation naechsnism is obtained Any ...

Cited by 14 - [Related articles](#) - All 3 versions

### Stacked PFET off-chip driver with a latch bias generator for overvoltage protection

F Hinedi, L Mamileti - US Patent 6,141,200, 2000 - Google Patents  
... of a processor or other device. 18 Claims, 2 Drawing Sheets DATAOUT ENABLE **IOPUT** GND Page 2. US Patent Oct. 31,2000 Sheet 1 of 2 ...

[Related articles](#) - All 2 versions

### Experience with CAD Tools for a 32-Bit VLSI Microprocessor

DR Ditzel, AD Berenbaum - Vlsi CAD Tools and Applications, 1987 - Kluwer Academic Publishers  
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### Multi-agent-systems based on coloured Petri nets- ▶ psu.edu [PDF]

D Moldt, F Wienberg - Lecture Notes in Computer Science, 1997 - Springer  
... with the Object-ID. A place **input** liin0t l 176 obj id" l method 1 l "l  
l method 2 "i r ..... methodn t ..... Figure 3: Folding ...

Cited by 91 - [Related articles](#) - BL Direct - All 8 versions

### Concurrent Hierarchical and Multilevel Simulation of VLSI Circuits

RB Mueller-Thuns, JT Rahmeh, JA Abraham, ... - SIMULATION, 1993 - sim.sagepub.com  
Page 1. http://sim.sagepub.com SIMULATION DOI: 10.1177/003754979306000202  
1993; 60; 79 SIMULATION Robert B. Mueller-Thuns, Joseph ...

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### 7.1 Direct Computation 7.2 Fast Fourisr Transform Algorithms 7.3 Recant Oevelopmants in ...

FF TRANSFORM - Modern Signal Processing: Proceedings of the Arab ..., 1986 - books.google.com  
... 1.2 Systeme If there in a usiqus outpet for acy givan input, the **input**-outpet ralatinship in called a system. A system T that relates ...

Circuit and method for voltage level translation utilizing a bias generator

F Hinedi, M Cases, S Dutta, RH Dennard - US Patent 5,867,010, 1999 - Google Patents

... the art. Resistor 110 couples clamp device 102 to an external signalling device via signal pad input, **IOPUT**, 112. Diode devices ...

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Software for device-independent graphical input

G Hamlin - 1982 - osli.gov

... Sosvctimec, however, the hpplication programmr mmy wjsh to Generate djfferetrt syntactic-level feedback for **iopot** fr- different devices. Our ...

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Did you mean: [input analog](#)

**DONALD: a workbench for interactive design space exploration and sizing of analog circuits**

K Swings, W Sansen - Design Automation. EDAC., Proceedings of the ..., 1991 - [ieeexplore.ieee.org](#)  
... circuit sizing. The program is de to numerically invert an analytic design model of **analog** circuit in any arbitrary direction. It ...

Cited by 25 - Related articles - All 4 versions

**7.1 Direct Computation 7.2 Fast Fourier Transform Algorithms 7.3 Recent Developments in ...**

FF TRANSFORM - Modern Signal Processing: Proceedings of the Arab ..., 1986 - [books.google.com](#)  
... in digital signal processing similar to exponential functions in **analog** signal processing ... in a unique output for any given input, the **input-output** relationship ...

**Application of chaos theory to digital transducer design**

PA Littlehales, CP Lewis, SR Bishop - Control, 1994. Control'94. International Conference on, 1994 - [ieeexplore.ieee.org](#)

... It may be used as an **analogue**. ... Conventional devices produce an **analogue** signal which has then to be converted to a digital format for processing. ...

BL Direct - All 3 versions

**An integrated mixed-mode neural network architecture for megasynapse ANNs**

J Schemmel, F Shurmann, S Hohmann, K ... - Neural Networks, 2002. IJCNN'02. Proceedings of the ..., 2002 - [ieeexplore.ieee.org](#)

... art approach in between these two opposite poles by combining fully **analog** network blocks ... the output at time t depends not only on the actual **input**, but also ...

Related articles

**Method of improving fault current measurement accuracy on electronic reclosure control**

PP Schumacher, JC Cummins - US Patent 5,701,080, 1997 - Google Patents

... DC current component (1\*), pursu- (M) 1aauaass fo appropriate selector **input** to the ...  $rc/(2+i)^2+(i)^2$  selector inputs to fee eight-bit **analog** multiplexers (26 ...

Cited by 2 - Related articles - All 4 versions

**M-3-a multilevel mixed-mode mixed A/D simulator**

R Chadha, C Visweswariah, CF Chen - IEEE Transactions on Computer-Aided Design of ..., 1992 - [ieeexplore.ieee.org](#)

... First, a methodology for describing arbitrary **analog** or mixed **analog/digital** blocks at the behavioral level is pro- posed. ... A. Describing an **Analog** Block ...

Cited by 9 - Related articles - All 3 versions

**rlrlrlrl, rrlrlrl**

T TT - Modern Signal Processing: Proceedings of the Arab ..., 1986 - [books.google.com](#)

... In many cases, the **input** data x (n) is very long, while the unit sample ... reading on this topic, see [U, 5], 8. DIGITAL PROCESSING OF **ANALOG** SIGNALS Processing ...

**NETHDL: abstraction of schematics to high-level HDL**

D Fischer, Y Levhari, G Singer - ... Automation Conference. 1990. EDAC. Proceedings of ..., 1990 -

ieeexplore.ieee.org

... Conventional circuit (**analog**) simulators (SPICE [1], ASTAP [23] ) are not suited for the ... type, size and strength of devices, and INPUT/OUTPUT/ **IOPUT** pins for ...

Cited by 3 - Related articles - All 3 versions

### Very high frequency regulator for space applications

A Capel, D O'Sullivan - Power Electronics Specialists Conference, 1996. PESC ..., 1996 - ieeexplore.ieee.org

... which can hardly be implemented at conventional frequency using **analog** technology, but will be impossible to install at 30 MHz. ...

Cited by 6 - Related articles - BL Direct

### A systematic approach to the control of electrohydraulic servosystems


A Alleyne - American Control Conference, 1998. Proceedings of ..., 1998 - ieeexplore.ieee.org

... zero. Hence the output tracking error,  $e(t)$ ,  $\dot{e}(t) = -k_e e(t)$ , (2.22)

$e(t) = y - Y_{desired}$ , converges to zero as  $t \rightarrow \infty$ . The ...

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### A 12-bit sigma-delta **analog-to-digital** converter with a 15-MHz clock rate

R Koch, B Heise, F Eckbauer, E Engelhardt, JA ... - IEEE Journal of Solid-State Circuits, 1986 - [ieeexplore.ieee.org](#)

... a new integrator concept was developed using **bidirectional** current sources ... was developed as the **analog** front end ... IN an integrated services **digital** network (ISDN ...

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### [CITATION] 256-channel **bidirectional** optical interconnect using VCSELs and photodiodes on CMOS

DV Plant, MB Venditti, E Laprise, J Faucher, ... - Journal of Lightwave Technology, 2001 - OSA

Cited by 90 - Related articles - BL Direct - All 11 versions

### In vivo **bidirectional** color Doppler flow imaging of picoliter blood volumes using optical ...

► [arizona.edu](#) [PDF]

JA Izatt, MD Kulkarni, S Yazdanfar, JK Barton ... - Optics Letters, 1997 - OSA

... Letter we describe the application of this technique to **bidirectional** blood flow mapping in ... BPF, bandpass filter; A/D's, **analog-digital** converters. ...

Cited by 246 - Related articles - BL Direct - All 13 versions

### Current-steering **digital-to-analog** converter for providing bi-directional currents through a ...

R Hallgren - US Patent 4,683,458, 1987 - Google Patents

... TO-ANALOG CONVERTER FOR PROVIDING BI-DIRECTIONAL CURRENTS THROUGH ... the field of **Digital-to- Analog** (D/A ... bit **digital** signals to **bidirectional analog** currents has ...

Cited by 12 - Related articles - All 7 versions

### Superconducting **analog-to-digital** converter with **bidirectional** counter

RR Phillips, RD Sandell, AH Silver - US Patent 4,848,060, 1987 - Google Patents

... 24, 1987 [54] SUPERCONDUCTING **ANALOG-TO-DIGITAL** CONVERTER WITH **BIDIRECTIONAL** COUNTER

[75] Inventors: Richard R. Phillips, Gardena; Robert D. Sandell, Manhattan ...

Cited by 13 - Related articles - All 6 versions

### A 64× 64 CNN universal chip with **analog** and **digital** I/O

S Espejo, R Domínguez-Castro, G Liñán, A ... - 1998 IEEE International Conference on Electronics, ..., 1998 - [ieeexplore.ieee.org](#)

... I, consists basically of two **bidirectional** data buses (one **digital** and one **analog**, both with a width of 16-lines), and a control bus comprising several ...

Cited by 58 - Related articles

### High speed **bidirectional digital** cable transmission system

TM Paff, H Graham - US Patent 5,553,064, 1996 - Google Patents

... said QPSK modulated data, and a **digital to analog** ... converting said QPSK modulated to **analog** form for ... 2. The high speed **bidirectional** cable transmission system ...

Cited by 20 - Related articles - All 6 versions

### Superconducting **analog-to-digital** converter with grounded four-junction squid **bidirectional**

GS Lee - US Patent 5,019,818, 1991 - Google Patents

... a converters and, more particularly, to high-speed, high- double-junction SQUID quantizer and a **bidirectional** resolution **analog-to-digital** converters employing ...

Cited by 10 - Related articles - All 5 versions

Boundary scan and its application to **analog-digital** ASIC testing ina board/system...

PP Fasang - ... Integrated Circuits Conference, 1989., Proceedings of ..., 1989 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... BS cell, the output BS cell, and the **bidirectional** BS cell. Then the paper explains the application of Boundary Scan to the testing of **analog-digital** ASKS in a ...

Cited by 7 - Related articles

... : An optimal architecture and learning technique for **analog** VLSI feedforward and recurrent

...

M Jabri, B Flower - Neural Computation, 1991 - MIT Press

... is shown in Figure 1. For fully parallel **analog** implementations of ... **Bi-Directional** ... flow for backpropagation training when fully **bidirectional** information paths ...

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Google scholar | bidirectional analog digital simulation | 1980 - 2002 | Search

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### Neuromorphic analogue VLSI

R Douglas, M Mahowald, C Mead - Annual review of neuroscience, 1995 - Annual Reviews  
... The choice between **digital simulation** and **analogue** emulation depends on a number of factors, such as ease of implementation and the goal of the experiment. ...

Cited by 98 - Related articles - BL Direct - All 7 versions

### A 12-bit sigma-delta analog-to-digital converter with a 15-MHz clock rate

R Koch, B Heise, F Eckbauer, E Engelhardt, JA ... - IEEE Journal of Solid-State Circuits, 1986 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... achieve this increase, a new integrator concept was developed using **bidirectional** current sources. ... KOCH et al.: 12-BITSIGMA-DELTA ANALOG-TO-DIGITAL CONVERTER ...

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### A switch-level model and simulator for MOS digital systems

RE Bryant - IEEE Transactions on computers, 1984 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... **Simulator** for MOS Digital Systems ... the logical behavior of **digital** systems implemented in ... logic; dynamic and static storage; (**bidirectional**) pass transistors ...

Cited by 113 - Related articles - All 14 versions

### [CITATION] 256-channel bidirectional optical interconnect using VCSELs and photodiodes on CMOS

DV Plant, MB Venditti, E Laprise, J Faucher, ... - Journal of Lightwave Technology, 2001 - OSA

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### Boundary scan and its application to analog-digital ASIC testing in a board/system

PP Fasang - ... Integrated Circuits Conference, 1989., Proceedings of ..., 1989 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... FIGURE 5. **BIDIRECTIONAL** BOUNDARY-SCAN CELL ShiftDR ... 3. INPUT BOUNDARY-SCAN CELL 4.

**Analog-Digital** ASICs ... control the **digital** circuit with **digital** test patterns ...

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### [BOOK] Mixed-mode simulation and analog multilevel simulation

RA Saleh, SJ Jou, AR Newton - 1994 - [books.google.com](http://books.google.com)

... systems in the time-domain. A complete hierarchy of modeling and **simulation** methods for **analog** and **digital** circuits is described. Fig. ...

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### Verilog-AMS: Mixed-signal simulation and cross domain connect modules- ► [columbia.edu](http://columbia.edu)

[PDF]

P Frey, DO'Riordan - ... Workshop on Behavioral Modeling and **Simulation**, 2000 - [doi.ieeecomputersociety.org](http://doi.ieeecomputersociety.org)  
... as **analog-to-digital** converters, **digital-to-analog** converters, or **bidirectional** converters, cause ... Rule 9 Any **digital** nets that are connected to the ...

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### Pseudo-Boolean logic circuits- ► [umich.edu](http://umich.edu)

JP Hayes - IEEE Transactions on Computers, 1986 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... as transistor switches, pullup loads, **bidirectional** buses, and the like [2]. **Analog** simulators such as ... time when applied to very large **digital** circuits. ...

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**Event-driven behavioural simulation of analogue transfer functions**

RA Cottrell - ... Automation Conference, 1990. EDAC. Proceedings of ..., 1990 - [ieeexplore.ieee.org](#)

... the **simulator** could support such **bidirectional** transfer of ... of Analorme Transfer Function

Modelling **Analogue** transfer function ... is used in **digital** circuit design ...

Cited by 9 - Related articles - All 4 versions

**[PDF] ► Evolution of continuous-time modeling and simulation**

KJ Åström, H Elmqvist, SE Mattsson - Proceedings of the 12th European **Simulation** ..., 1998 - [diquima.upm.es](#)

... **Analog** techniques were predominant from 1920 to 1950. Major changes took place when

**digital** computers were available and **simulation** techniques have then ...

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[BOOK] **Mixed-mode simulation and analog multilevel simulation**

RA Saleh, SJ Jou, AR Newton - 1994 - books.google.com

... Page 22. **MIXED-MODE SIMULATION** This book addresses the problems of simulating entire **mixed analog/digital** systems in the time-domain. ...

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[BOOK] **Switched-currents: an analogue technique for digital technology**

C Toumazou, JB Hughes, NC Battersby - 1993 - books.google.com

... 7 **Bi-directional** calibrating current cell ... are advantageous from both economic and systems design viewpoints, combining both **analogue** and **digital** circuits on ...

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Verilog-AMS: **Mixed-signal simulation and cross domain connect modules-** ► [columbia.edu](http://columbia.edu)

[PDF]

P Frey, DO'Riordan - ... Workshop on Behavioral Modeling and **Simulation**, 2000 - doi.ieeecomputersociety.org

... as **analog-to-digital** converters, **digital-to-analog** converters, or **bidirectional** converters, cause ... Rule 9 Any **digital** nets that are connected to the ...

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Evaluating **mixed-signal** simulators

D Overhauser, R Saleh - ... Integrated Circuits Conference, 1995., Proceedings of ..., 1995 - ieeeexplore.ieee.org

... Section 111 discusses issues involved with a unidirectional **analog-to-digital** interface, Section IV discusses **bidirectional** interfacing issues ...

Cited by 8 - Related articles - All 2 versions

[BOOK] **Applications of VHDL to circuit design**

RE Harr, AG Stanculescu - 1991 - books.google.com

... gate as a network of **bi-directional** Switches 36 ... for UART Design 154 5.26 **Bidirectional** Modeling (for ... Describing the functionality of **analog** circuits including ...

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RA Cottrell - ... Automation Conference, 1990. EDAC. Proceedings of ..., 1990 - ieeeexplore.ieee.org

... the **simulator** could support such **bidirectional** transfer of ... of Analorme Transfer Function Modelling **Analogue** transfer function ... is used in **digital** circuit design ...

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[PDF] ► ... Architecture Modeling Design Method for **Mixed Signal and Multi Domain System Simulation-** ...

M Schlegel, G Herrmann, D Müller - Proceedings of the International MEMS Workshop, ..., 2001 - tu-chemnitz.de

... order to describe the **analogue** behavior correctly ... information including the events controlling the **digital simulator**. ... may cause problems at **bi-directional** ports ...

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Using VHDL for high-level, **mixed-mode system simulation**

MB Srivastava, RW Brodersen - IEEE Design & Test of Computers, 1992 - ieeeexplore.ieee.org

... aided software engineering, and **digital** signal processing ... Electromechanical interfaces

**Analog** power electronics Robot ... flow unidirection- al or **bidirectional**? ...

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Boundary scan and its application to **analog-digital** ASIC testing ina board/system...

PP Fasang - ... Integrated Circuits Conference, 1989., Proceedings of ..., 1989 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)  
... **FIGURE 5. BIDIRECTIONAL BOUNDARY-SCAN CELL ShiftDR** ... in parallel, the **digital** patterns  
used in the logic **simulation** of the ... **FIGURE 6. ANALOG - DIGITAL ASIC WITH** ...

Cited by 7 - Related articles

[PDF] ► **Code-level modeling in XSPICE**

F Cox, W Kuhn, J Murray, S Tynor - Proceedings of the 1992 Intl. Symp. on Circuits and ..., 1992 - [eece.ksu.edu](http://eece.ksu.edu)  
... **Bidirectional** ports for modeling IV relationships. ... often contain both **analog** and  
**digital** components ... Traditional **analog simulation** methods such as those used in ...

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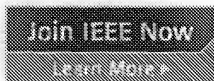
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- Zhang Xiaopin; Ye Peida;  
[TENCON 90, 1990 IEEE Region 10 Conference on Computer and Communic](#)  
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- Yin, H. B.; Wasson, H. M.;  
[Broadcast and Television Receivers, IRE Transactions on](#)  
 Volume BTR-3, Issue 2, Oct. 1957 Page(s):65 - 70  
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- ☐ 4. **Using CAD Tools in the Design of CRISP**
- Ditzel, D.R.; Berenbaum, A.D.:  
[Design & Test of Computers, IEEE](#)  
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#### 5. The Open Channel

Burkhardt, W.H.; Yuen, C.K.;  
 Computer  
 Volume 17, Issue 9, Sept. 1984 Page(s):119 - 120  
 Digital Object Identifier 10.1109/MC.1984.1659256  
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#### 6. M<sup>3</sup>-a multilevel mixed-mode mixed A/D simulator

Chadha, R.; Visweswariah, C.; Chen, C.-F.;  
 Computer-Aided Design of Integrated Circuits and Systems, IEEE Transaction  
 Volume 11, Issue 5, May 1992 Page(s):575 - 585  
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#### 7. Gesture-based programming for robotics: human-aug adaptation

Voyles, R.M.; Morrow, J.D.; Khosla, P.K.;  
 Intelligent Systems and Their Applications, IEEE [see also IEEE Intelligent Sy  
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#### 8. Implementation of a fast digital processor using the r system

Chao Huang; Peterson, D.; Rauch, H.; Teague, J.; Fraser, D.;  
 Circuits and Systems, IEEE Transactions on  
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#### 9. Continuous-time and switched capacitor monolithic f current and charge simulation

Haigh, D.G.; Taylor, J.T.; Singh, B.;  
 Circuits, Devices and Systems, IEE Proceedings G  
 Volume 137, Issue 2, April 1990 Page(s):147 - 155  
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#### 10. A multilevel-voltage source rectifier with a three-phas circuit as a main power circuit

OGUCHI, K.; MAKI, Y.;  
 Industry Applications Society Annual Meeting, 1992., Conference Record of I  
 4-9 Oct. 1992 Page(s):695 - 702 vol.1  
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11. **On designing robust testable PLA for path delay fault**

Gupta, B.; Rajsuman, R.;  
[Signals, Systems and Computers, 1989. Twenty-Third Asilomar Conference on](#)  
 Volume 2, Oct. 30-Nov. 1, 1989 Page(s): 999 - 1001

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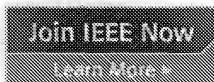


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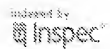
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